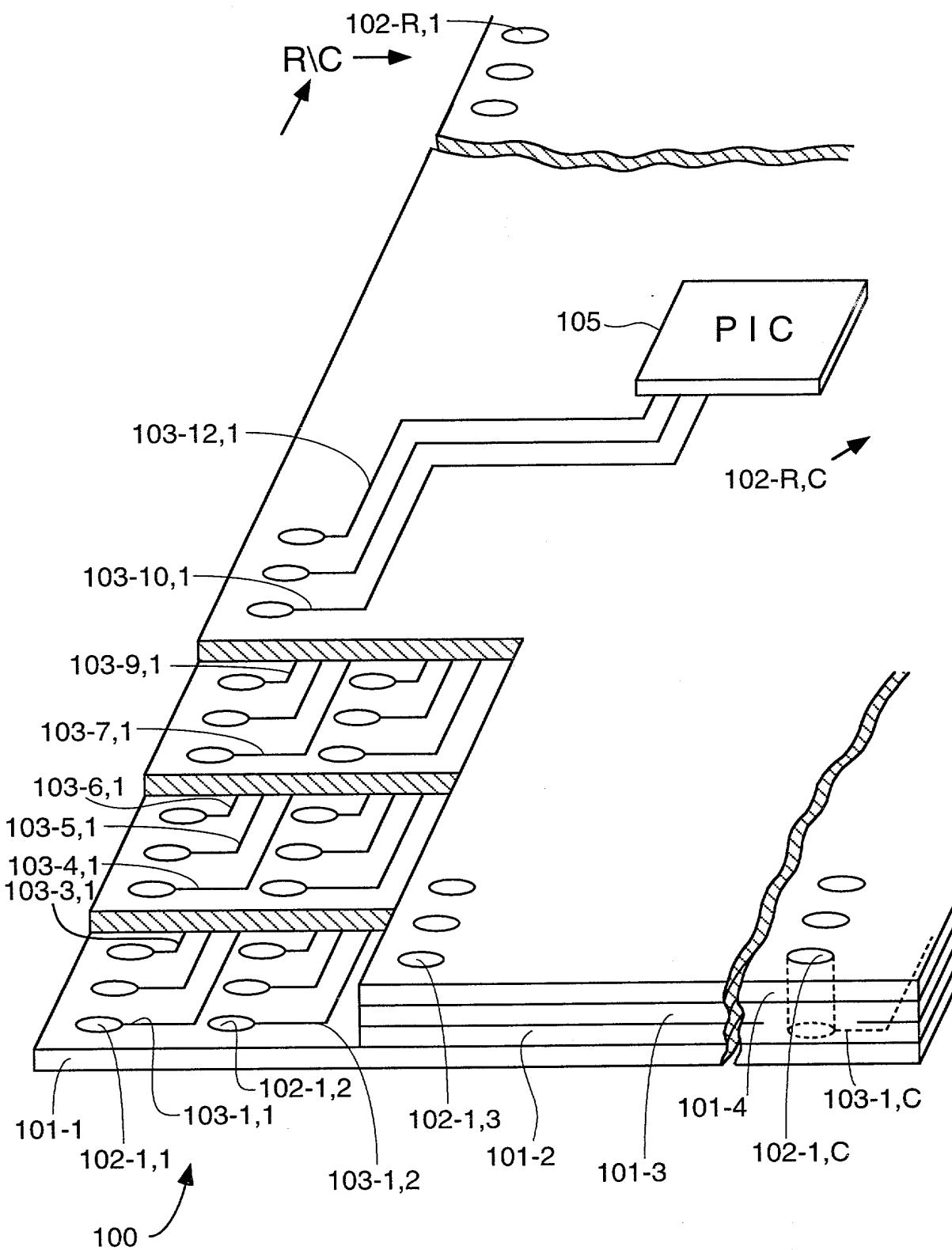


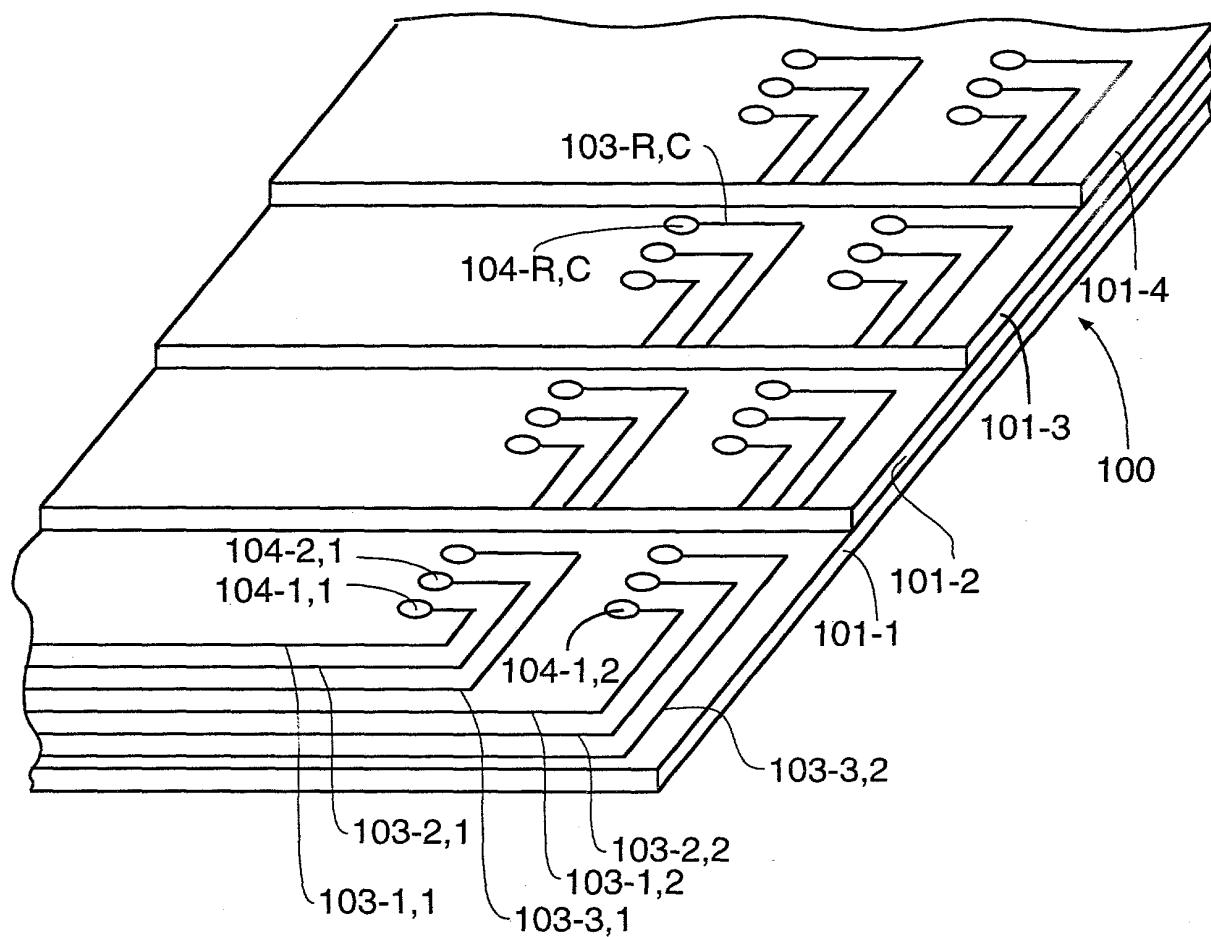
1/21

FIGURE 1a



2/21

FIGURE 1b



3/21

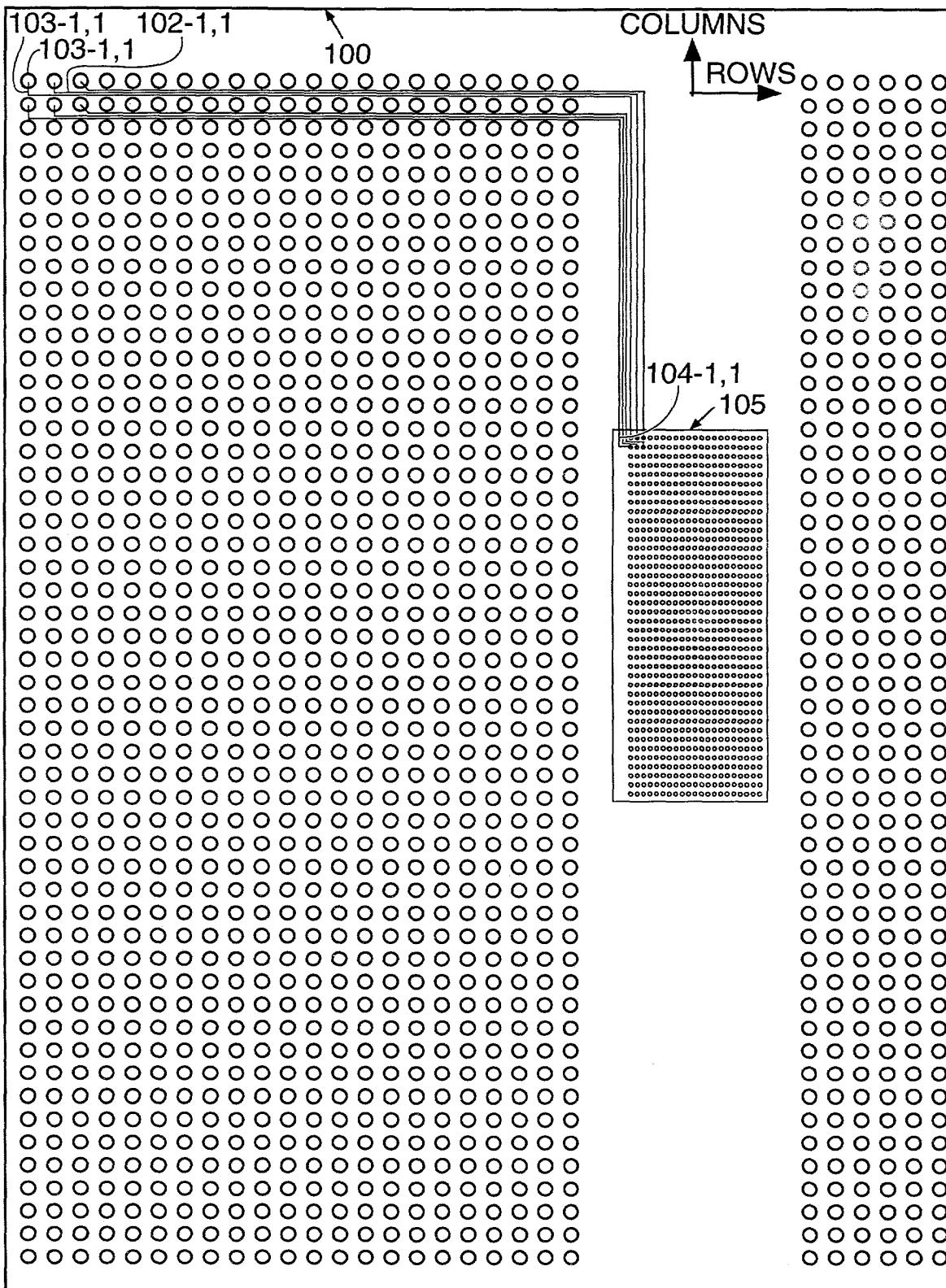


FIGURE 2a

4/21

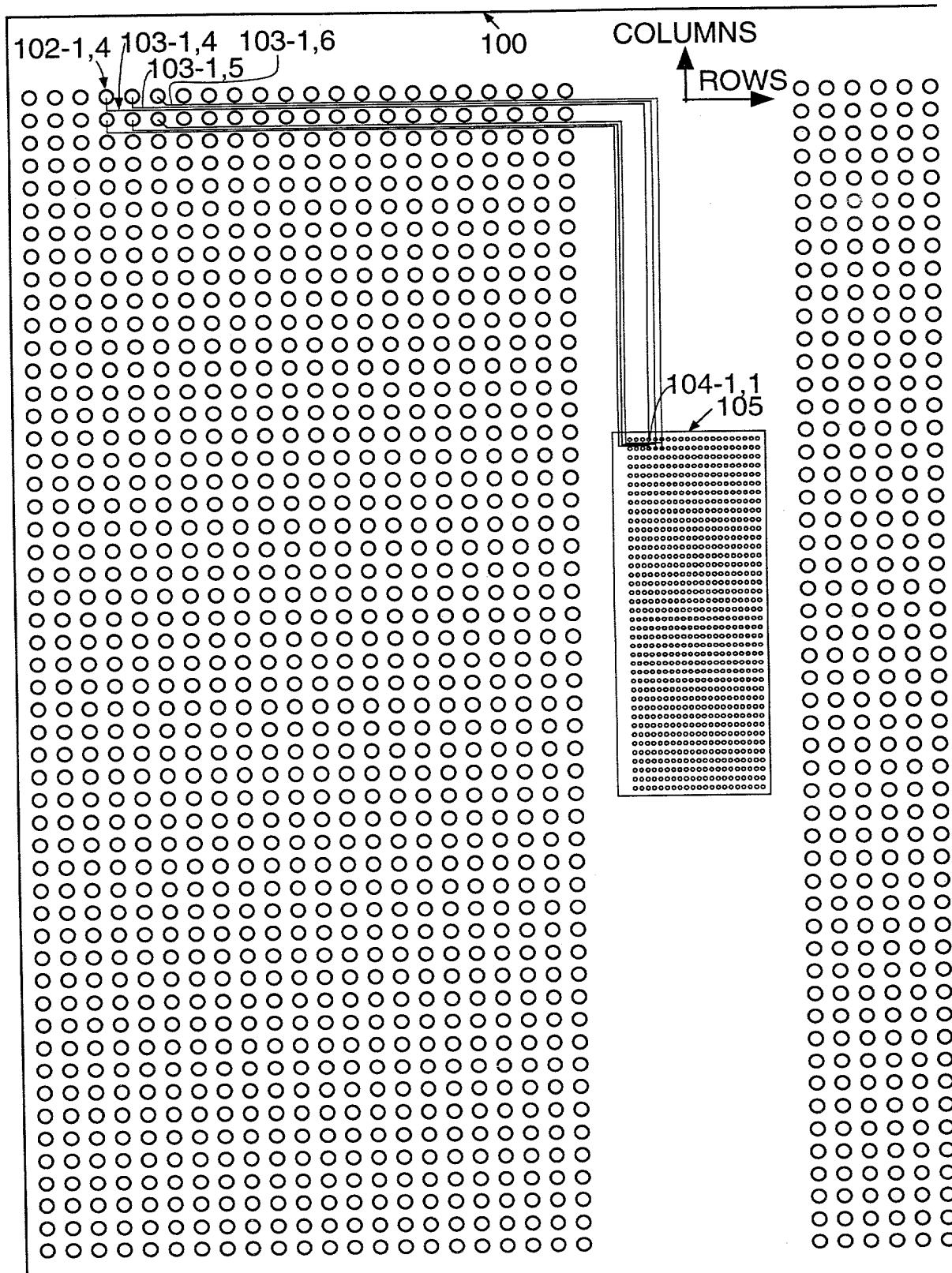


FIGURE 2b

10052631.02802

5/21

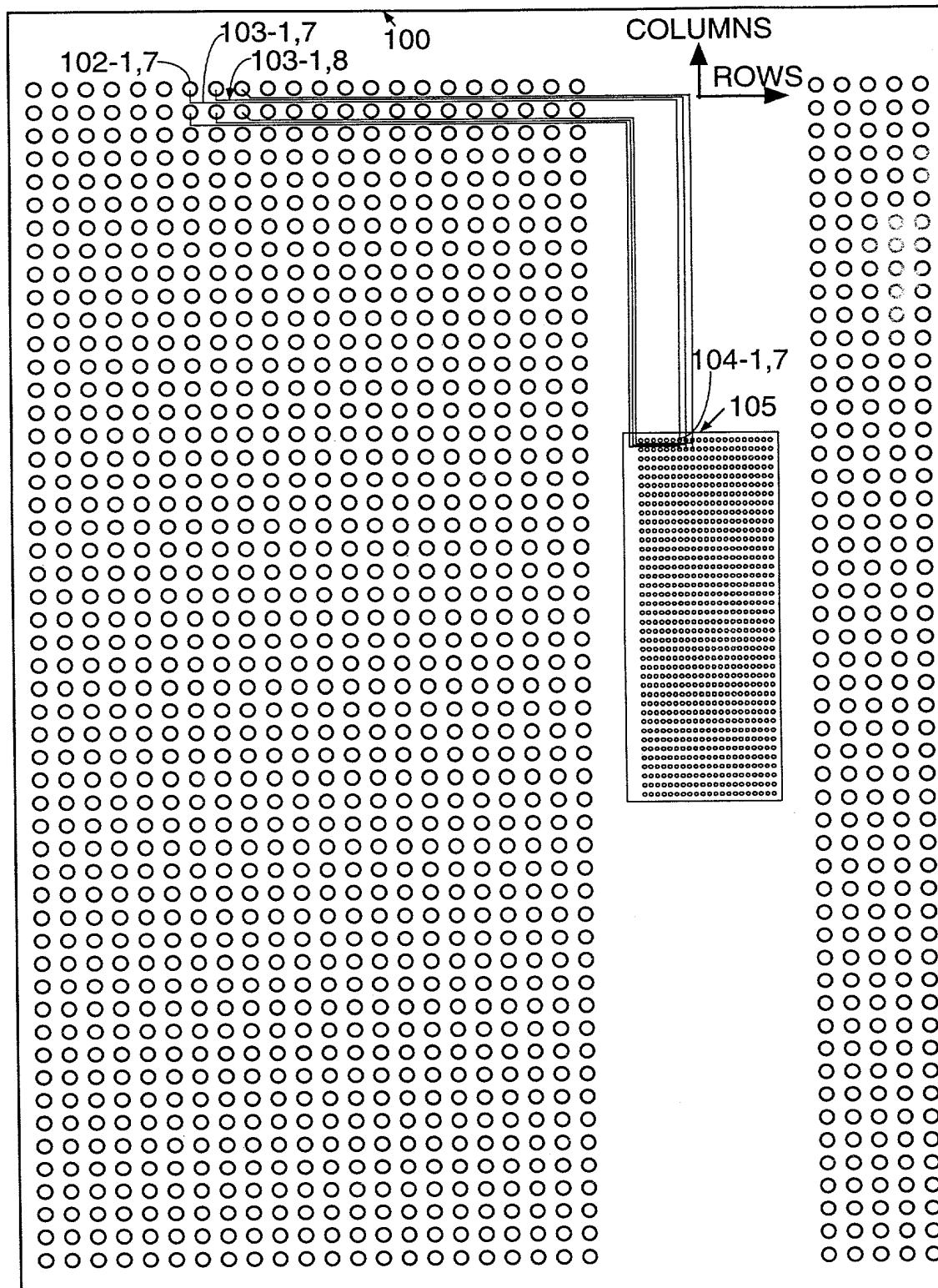


FIGURE 2c

6/21

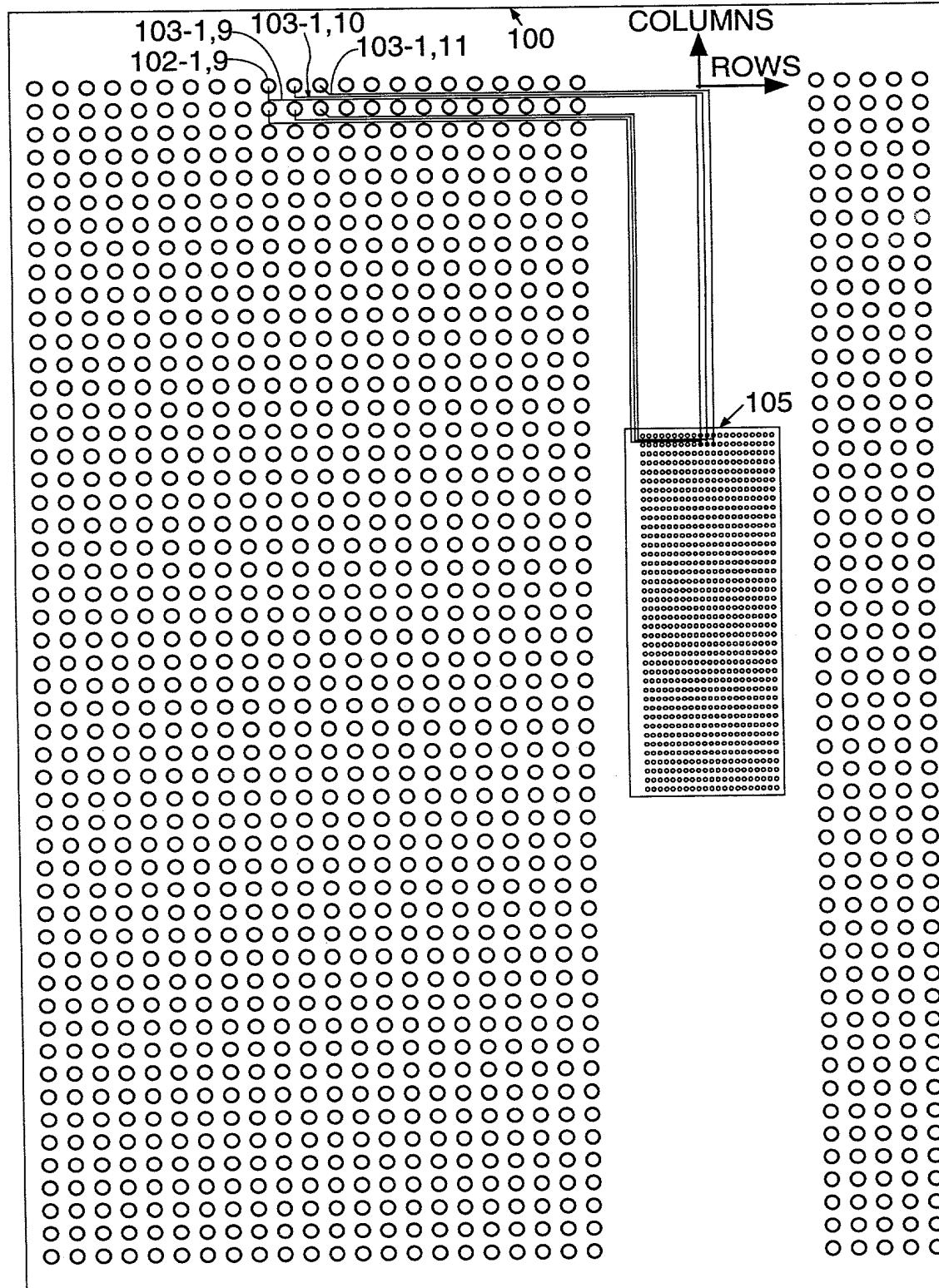
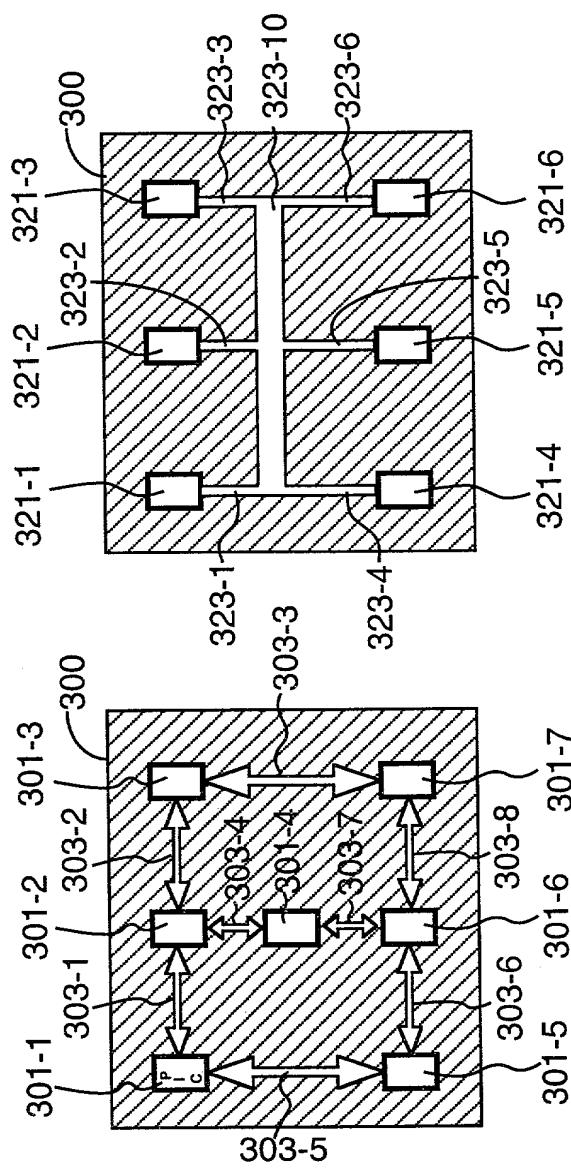


FIGURE 2d

GLOBAL INTERCONNECT ARCHITECTURE



NEXT NEIGHBOR

BUS

RANDOM FREEWAYS

7/21

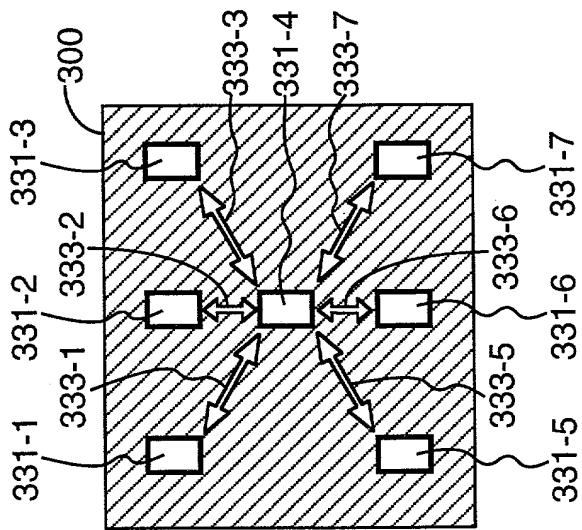


FIGURE 3a

FIGURE 3c

FIGURE 3d

Mohsen

8/21

HIERARCHY OF GLOBAL INTERCONNECTS

NEXT NEIGHBOR

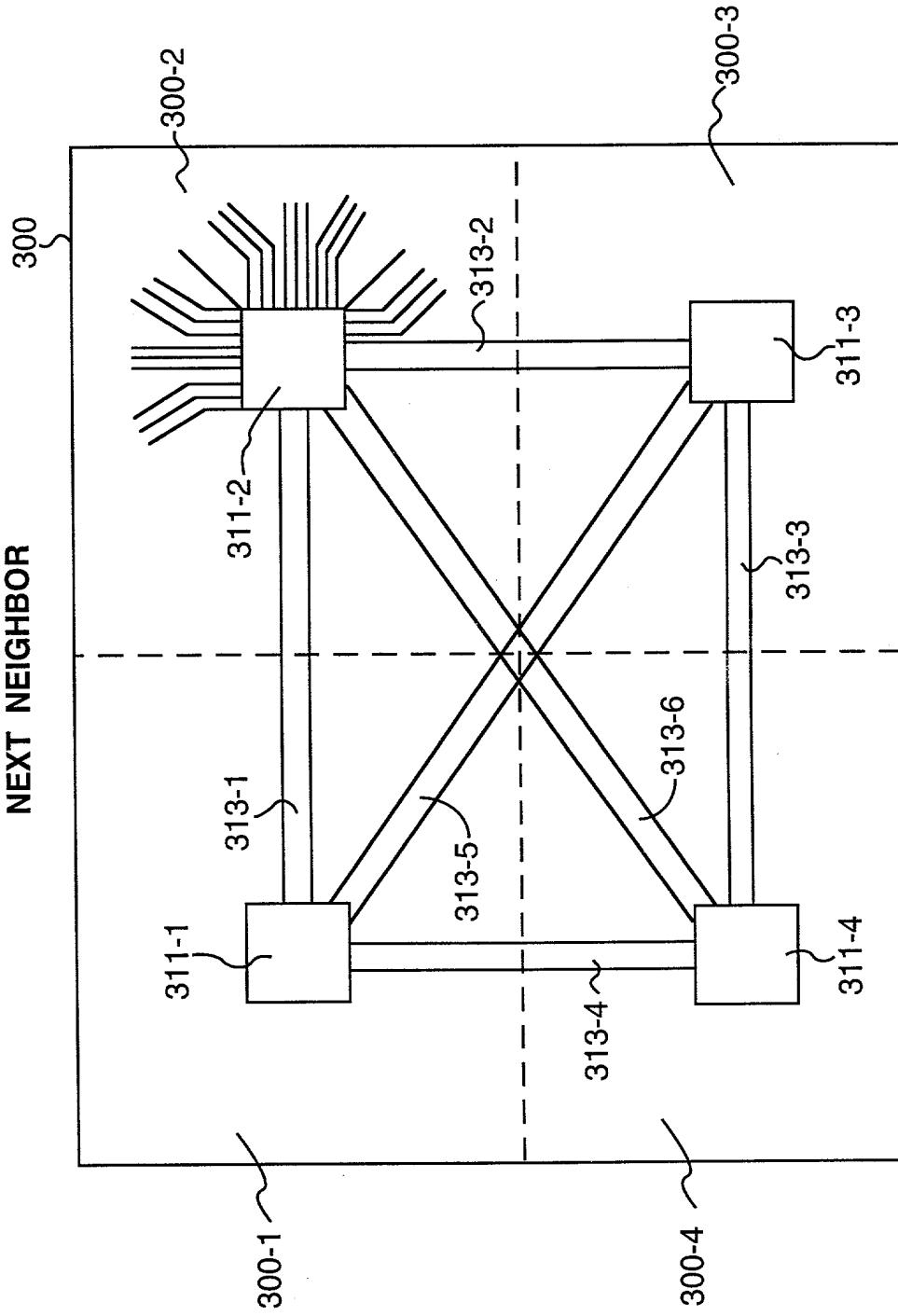


FIGURE 3b

9/21

FIELD PROGRAMMABLE PCB

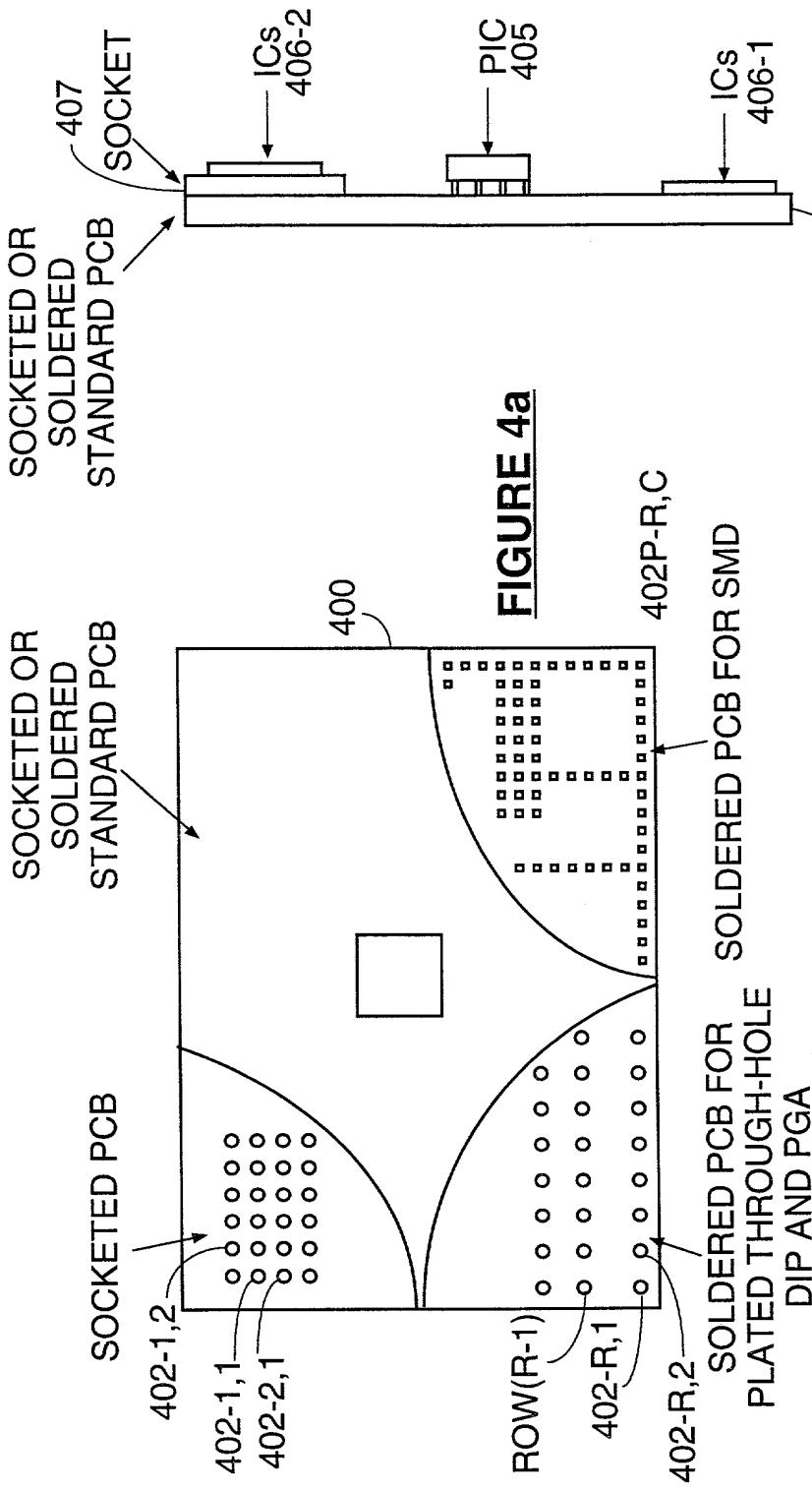


FIGURE 4a

402-R,2 / SOLDERED PCB FOR PLATED THROUGH-HOLE DIP AND PGA SOLDERED PCB FOR SMD



FIGURE 4C

FIGURE 4b

400

10/21

CONTROL PORT AND TEST PORT/ DIAGNOSTIC TOOL

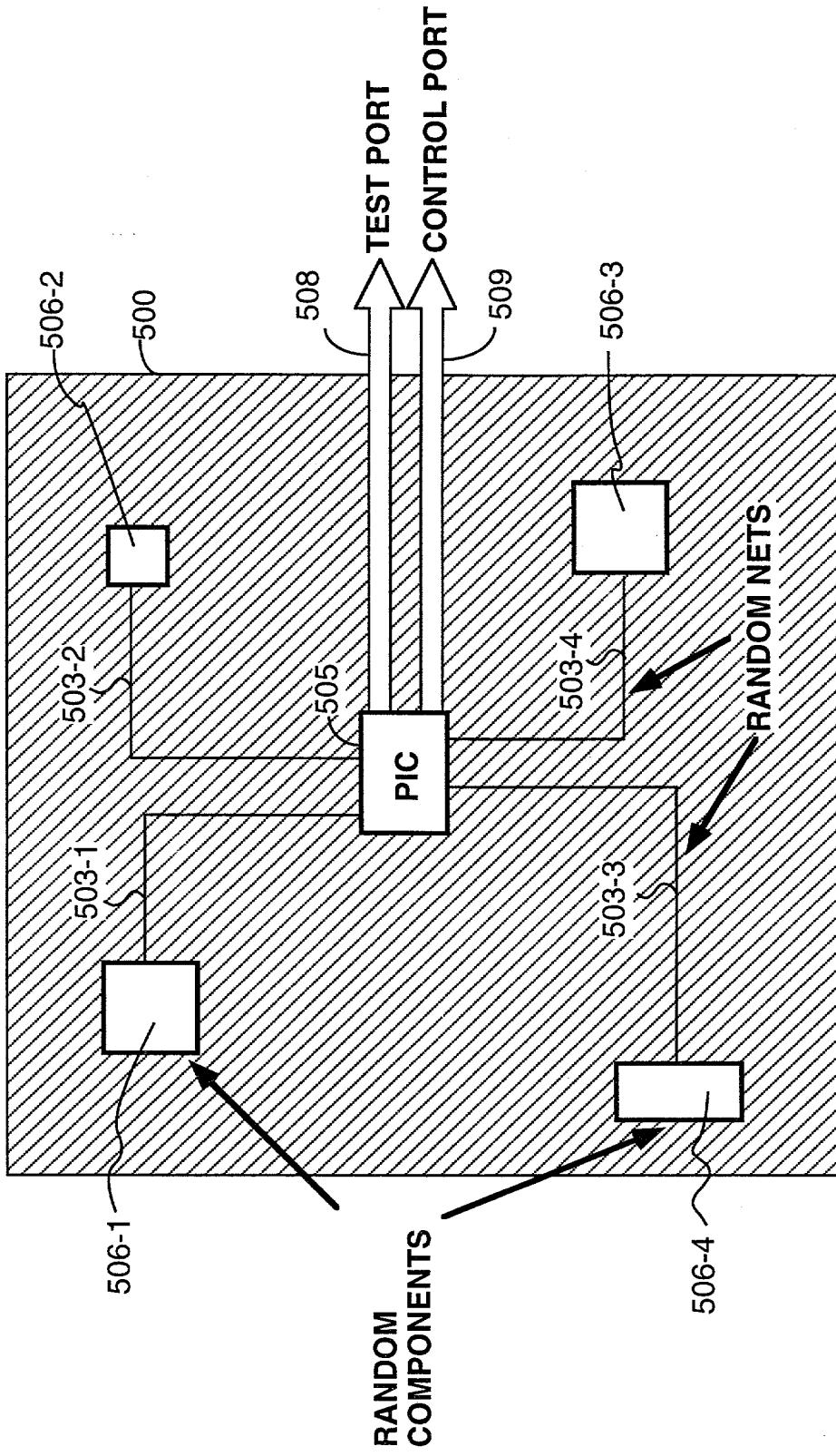


FIGURE 5

11/21

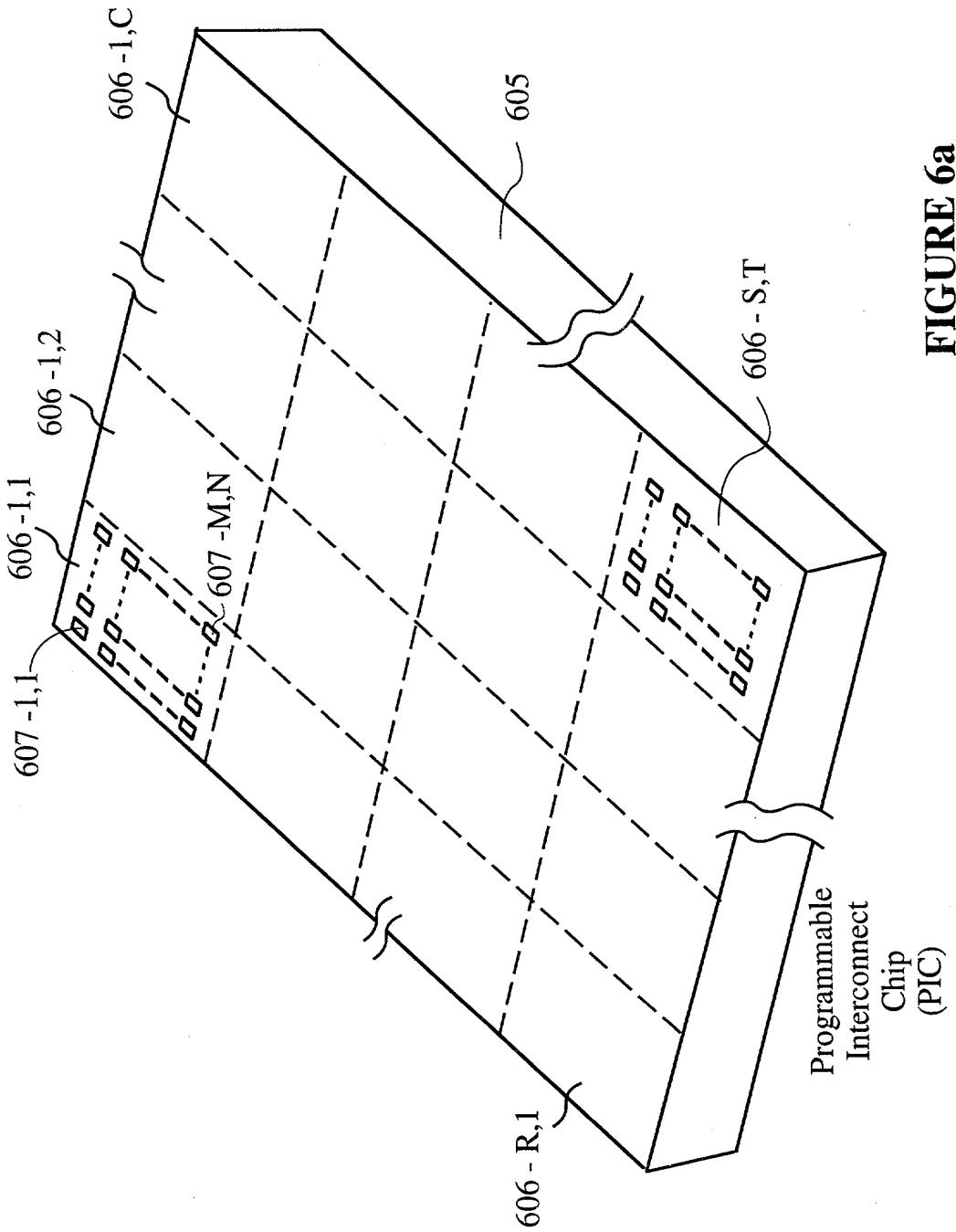


FIGURE 6a

12/21

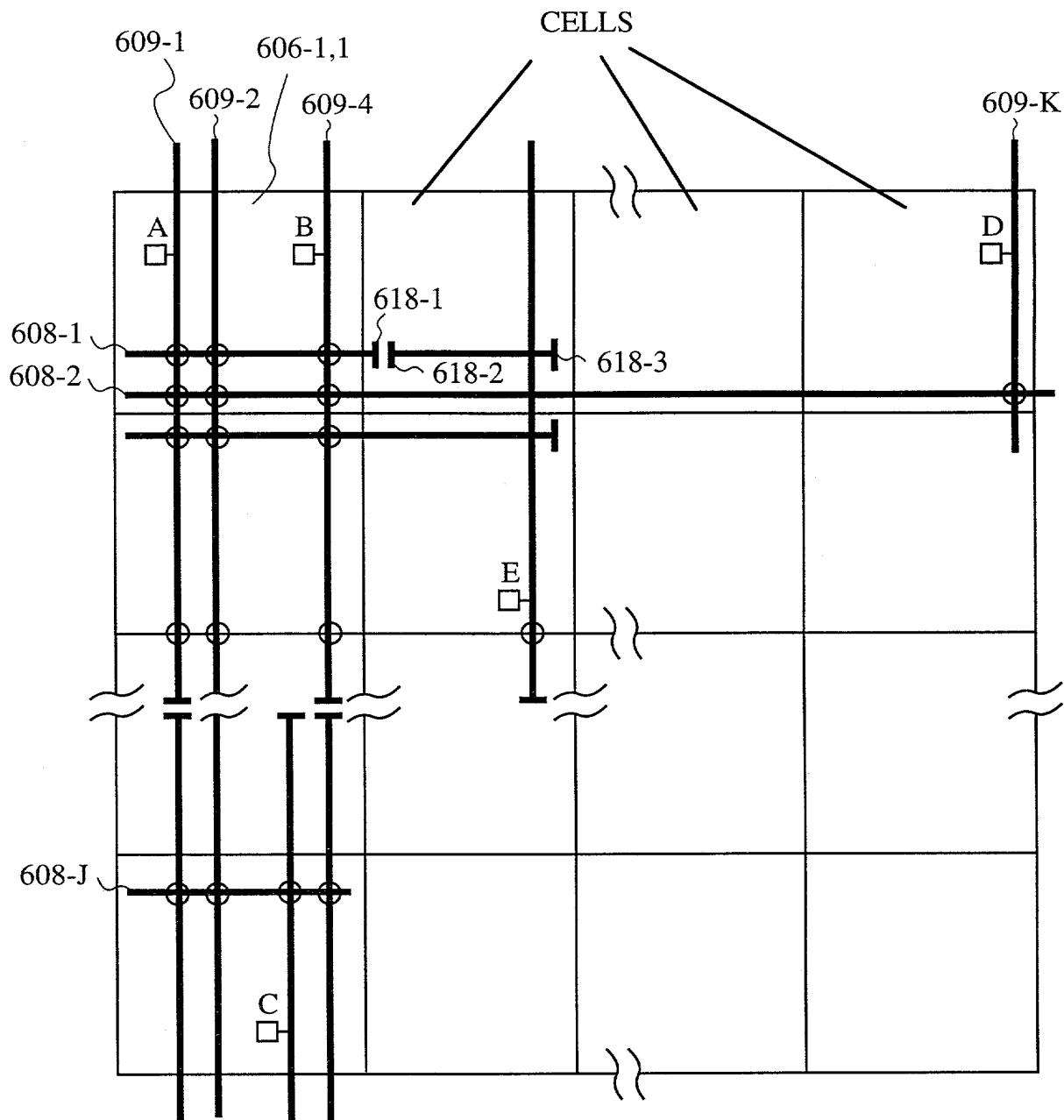
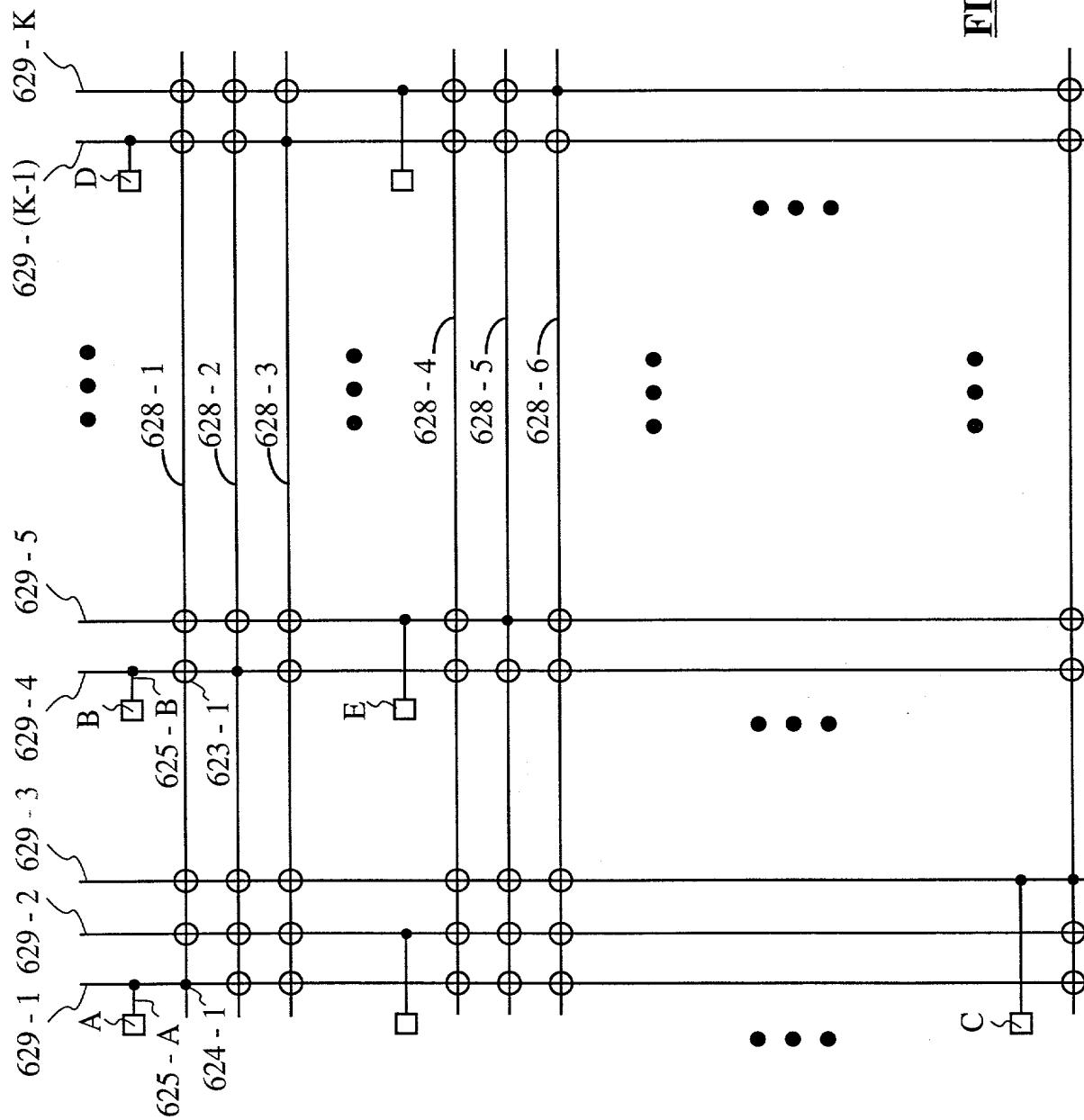


FIGURE 6b

13/21

FIGURE 6c



14/21

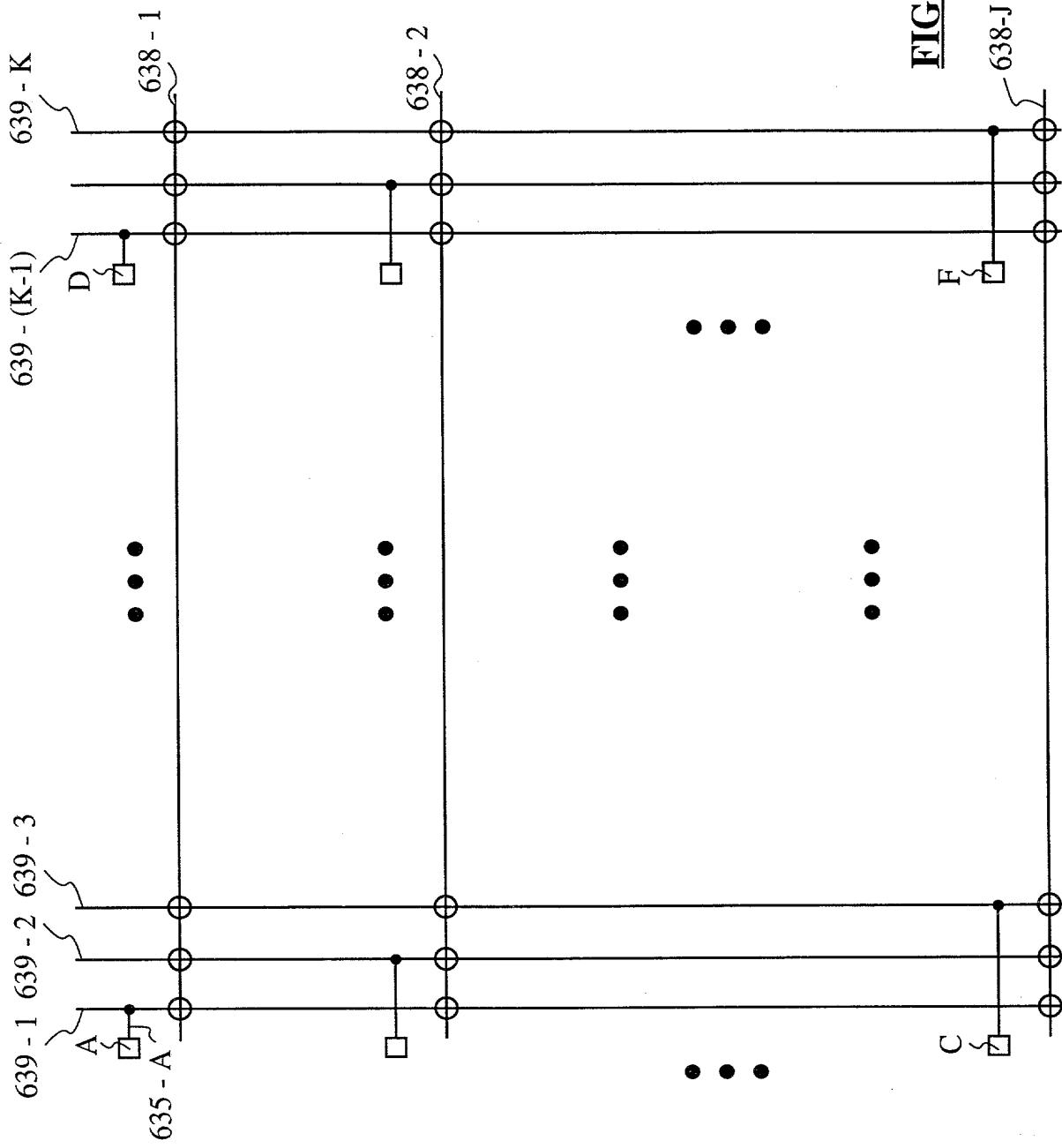
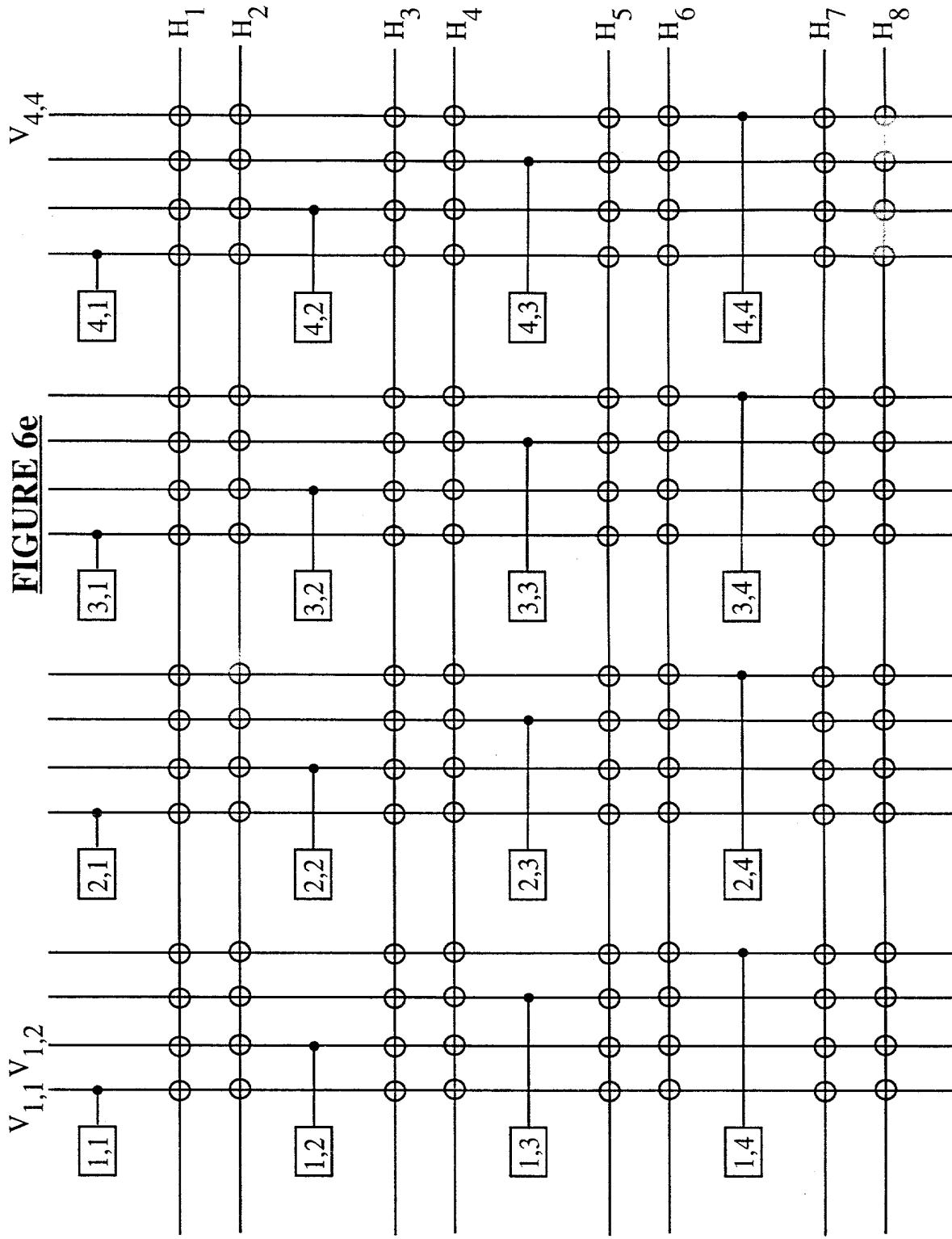
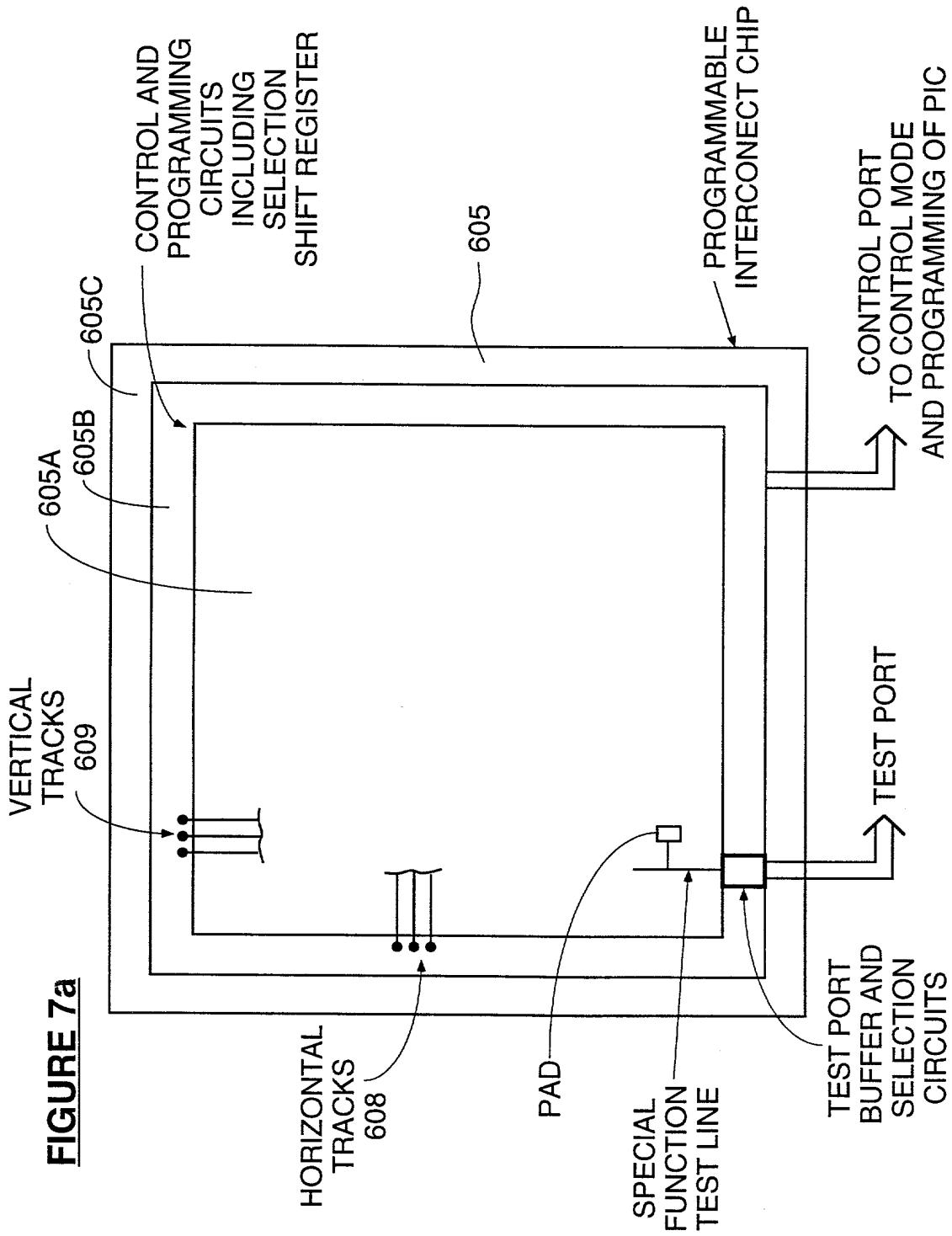


FIGURE 6e





17/21

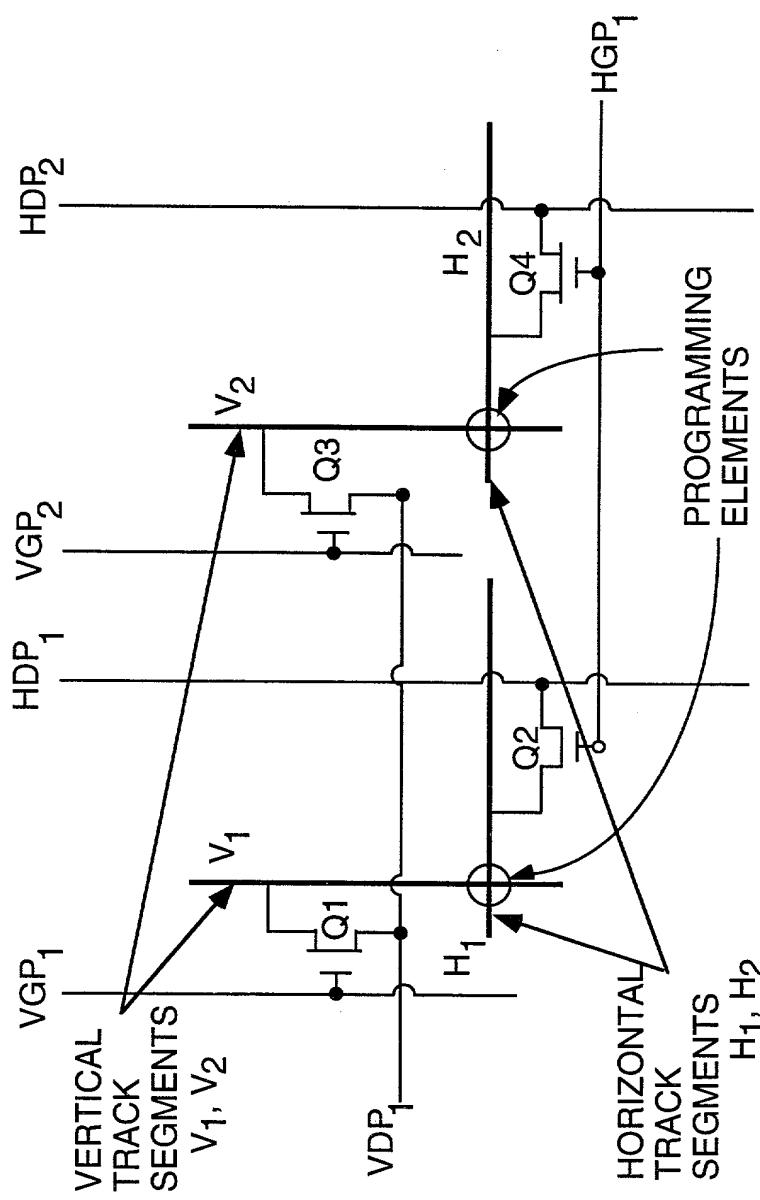


FIGURE 7b

18/21

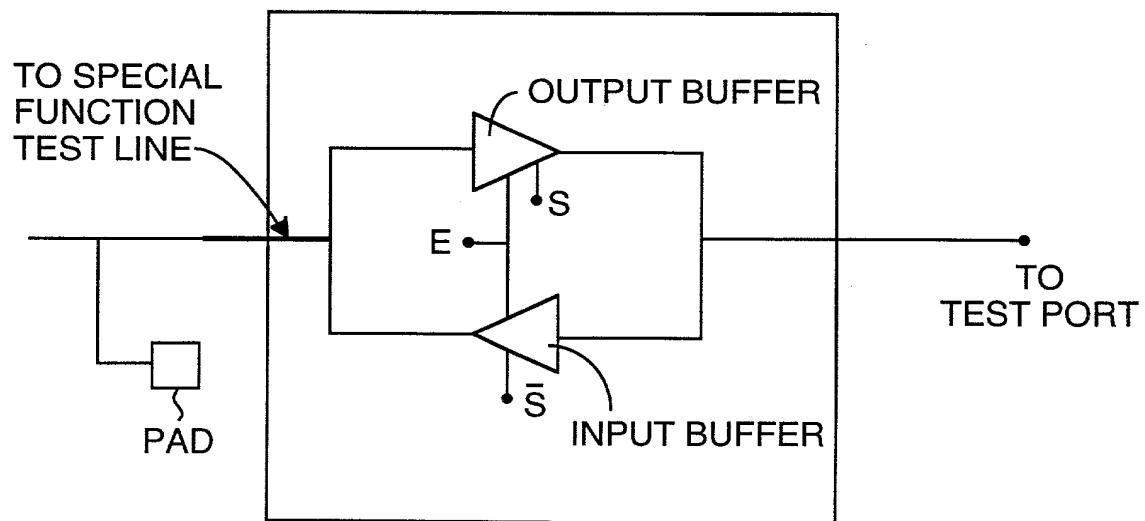
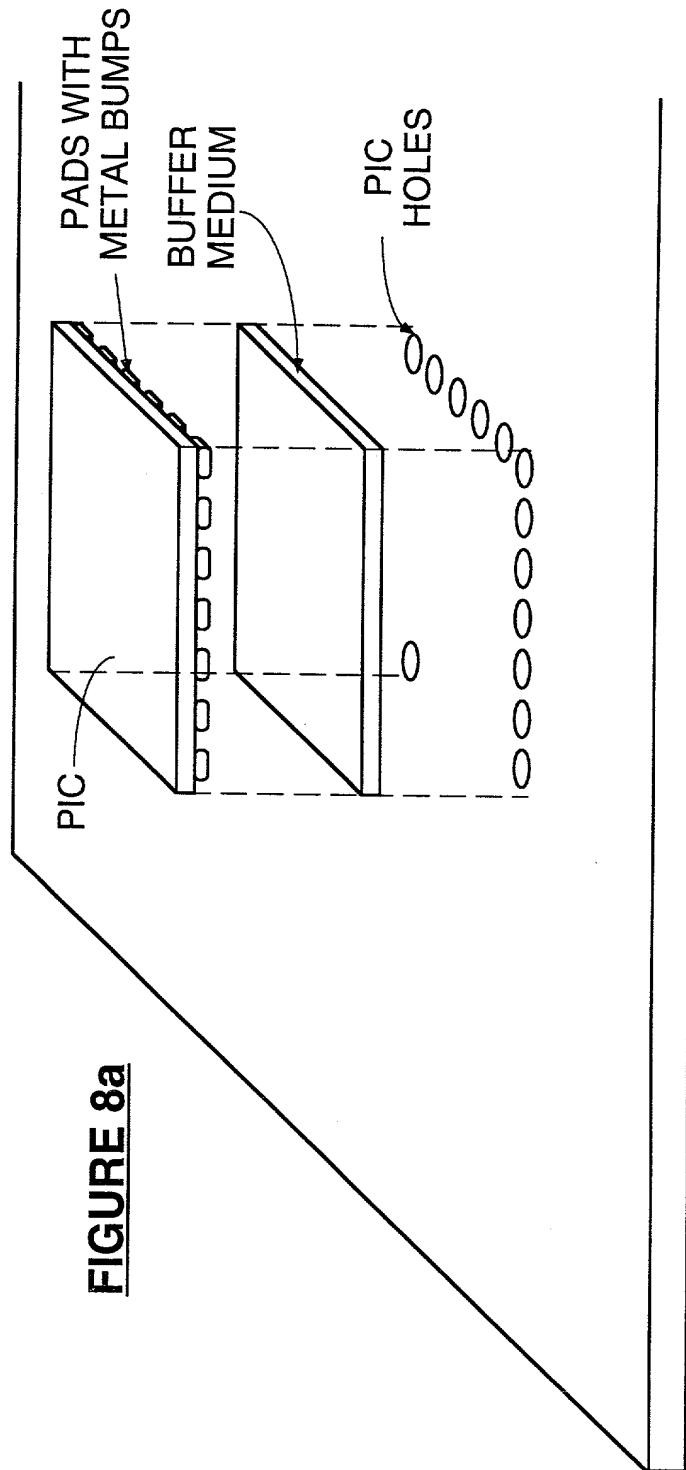


FIGURE 7c

19/21



20/21

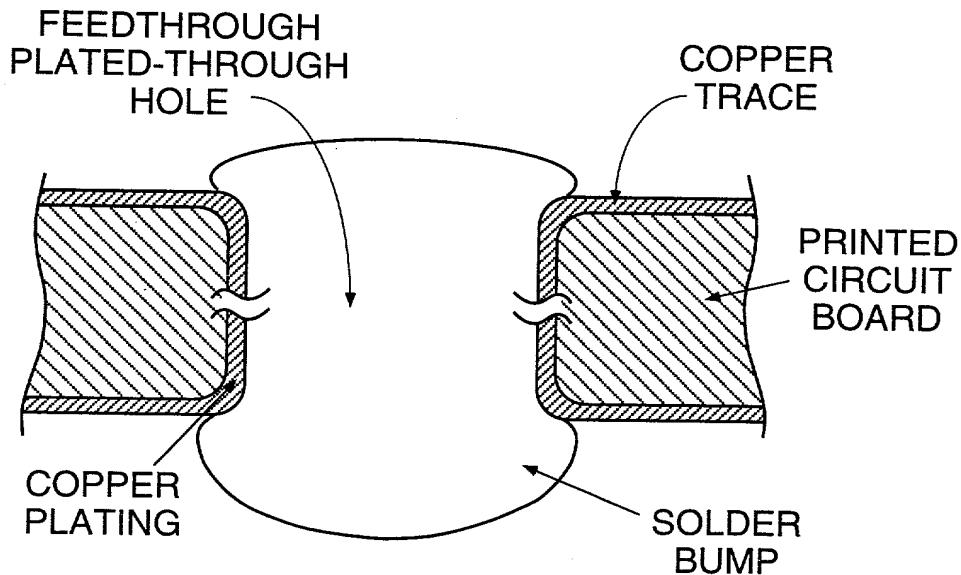


FIGURE 8b1

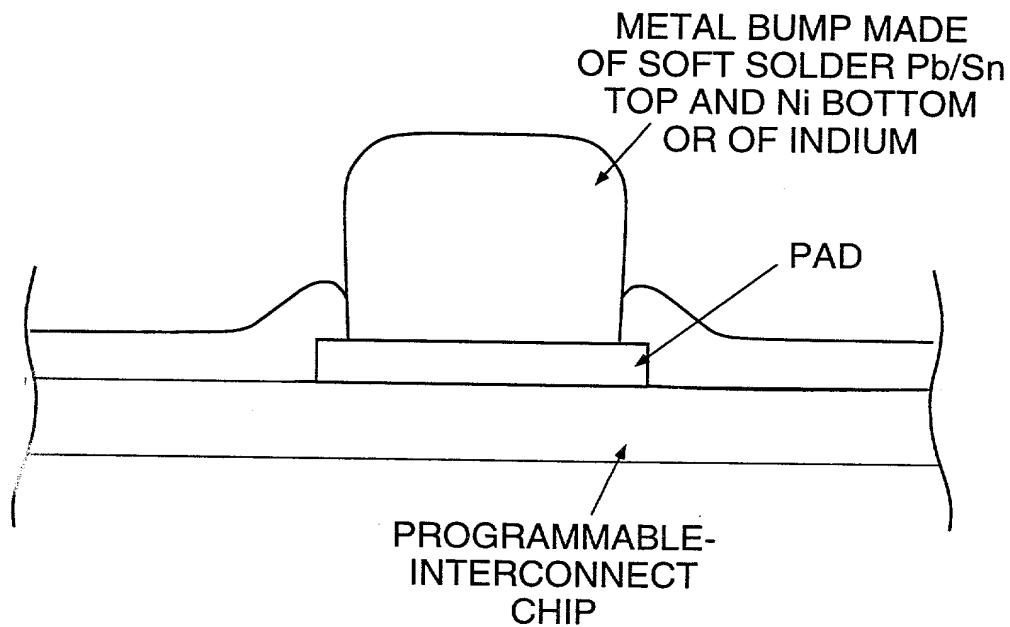
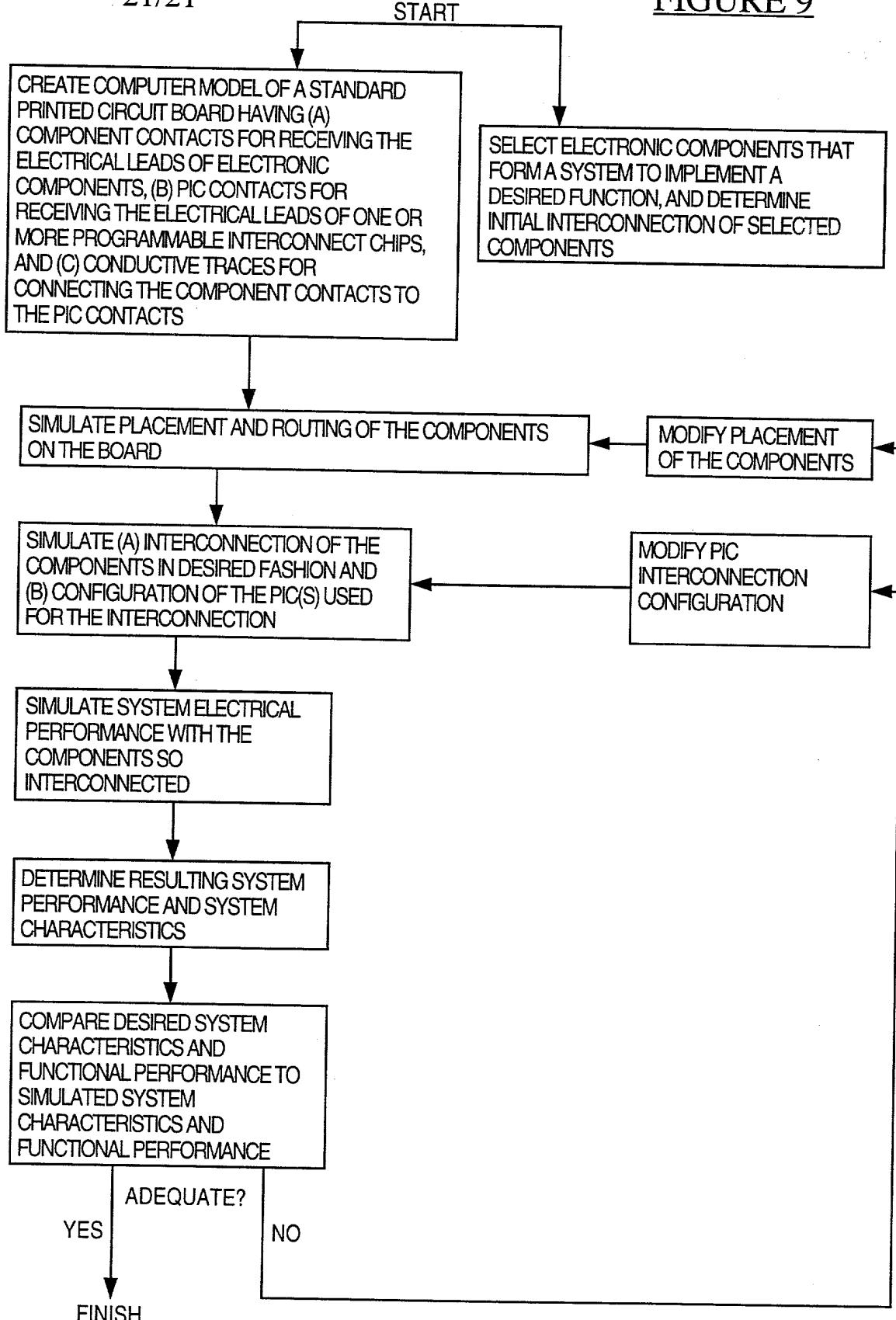


FIGURE 8b2

21/21

FIGURE 9



100052671-0328000